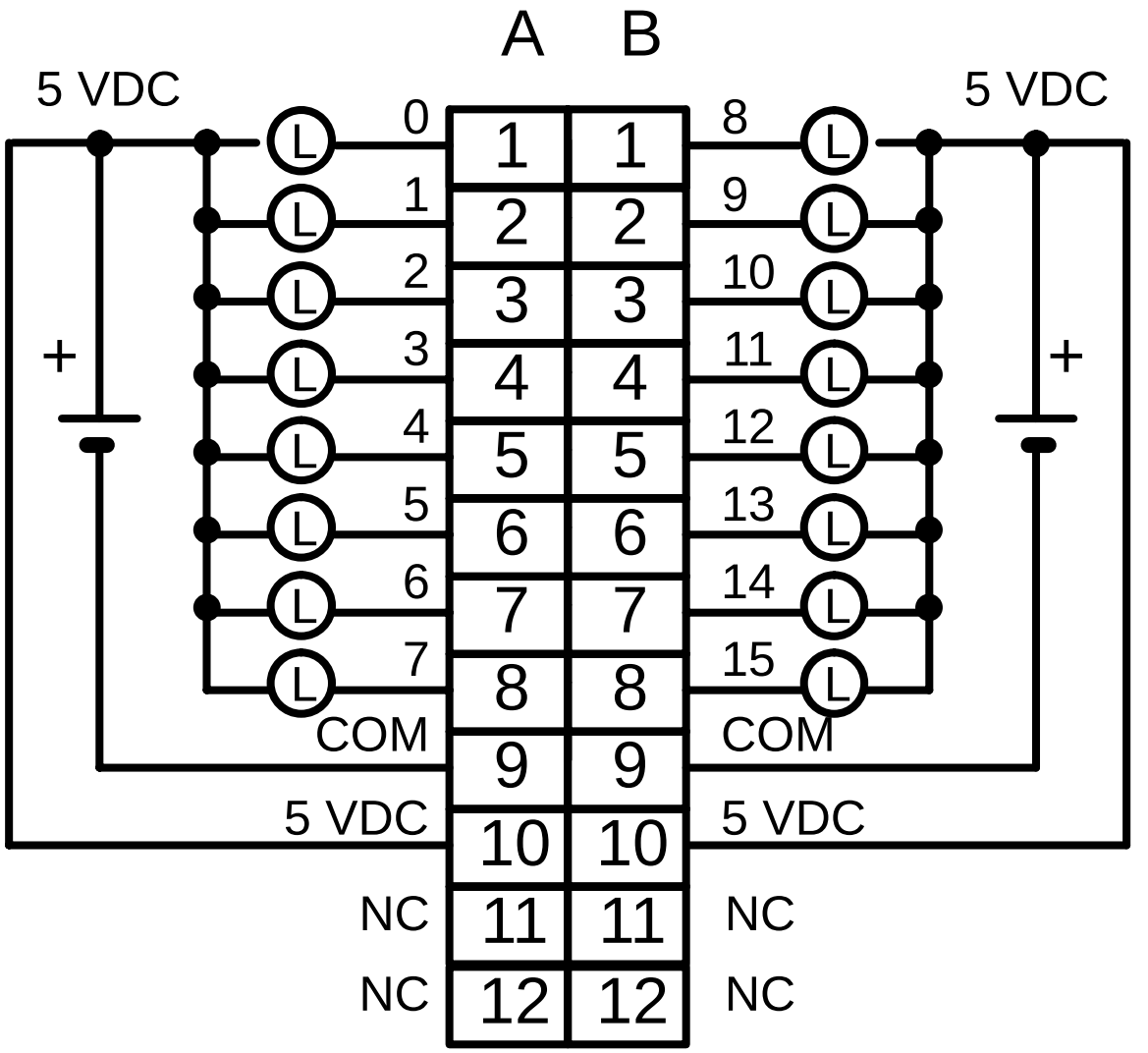
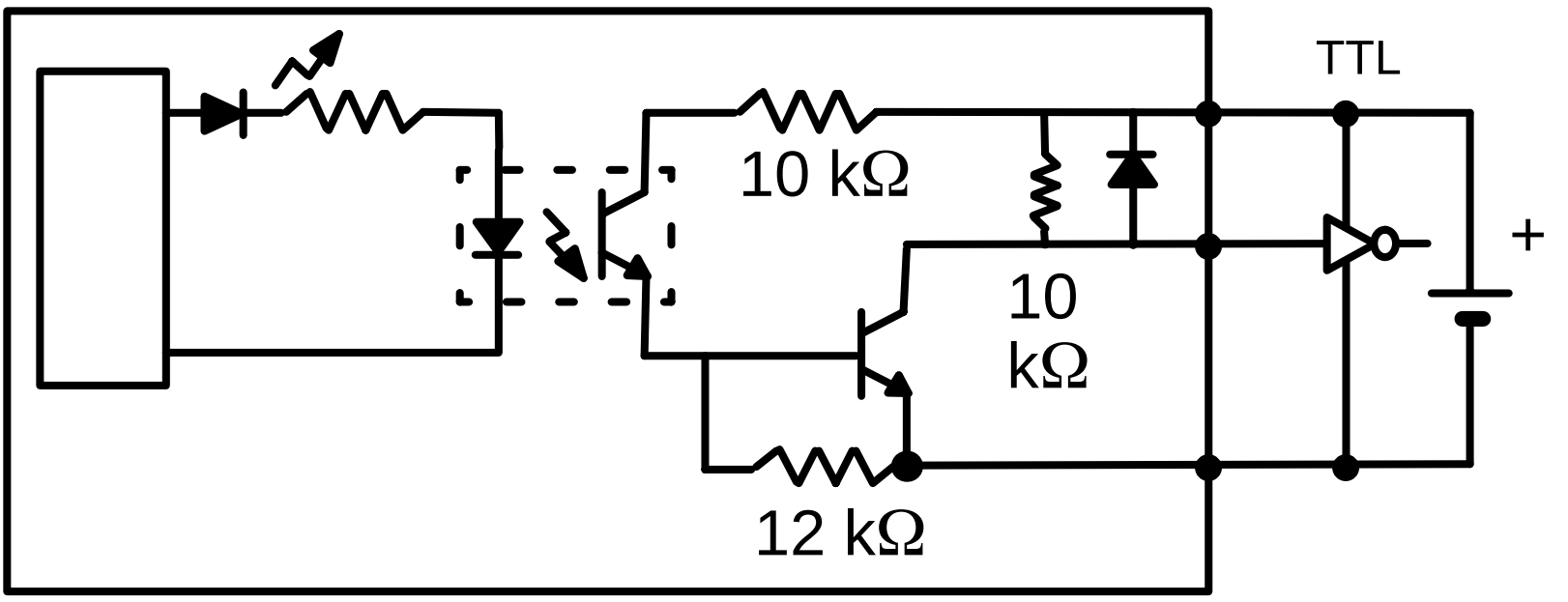
Because the output data is negative logic, the terminal output goes low when the output bit is ON (1).